

FIG. 1

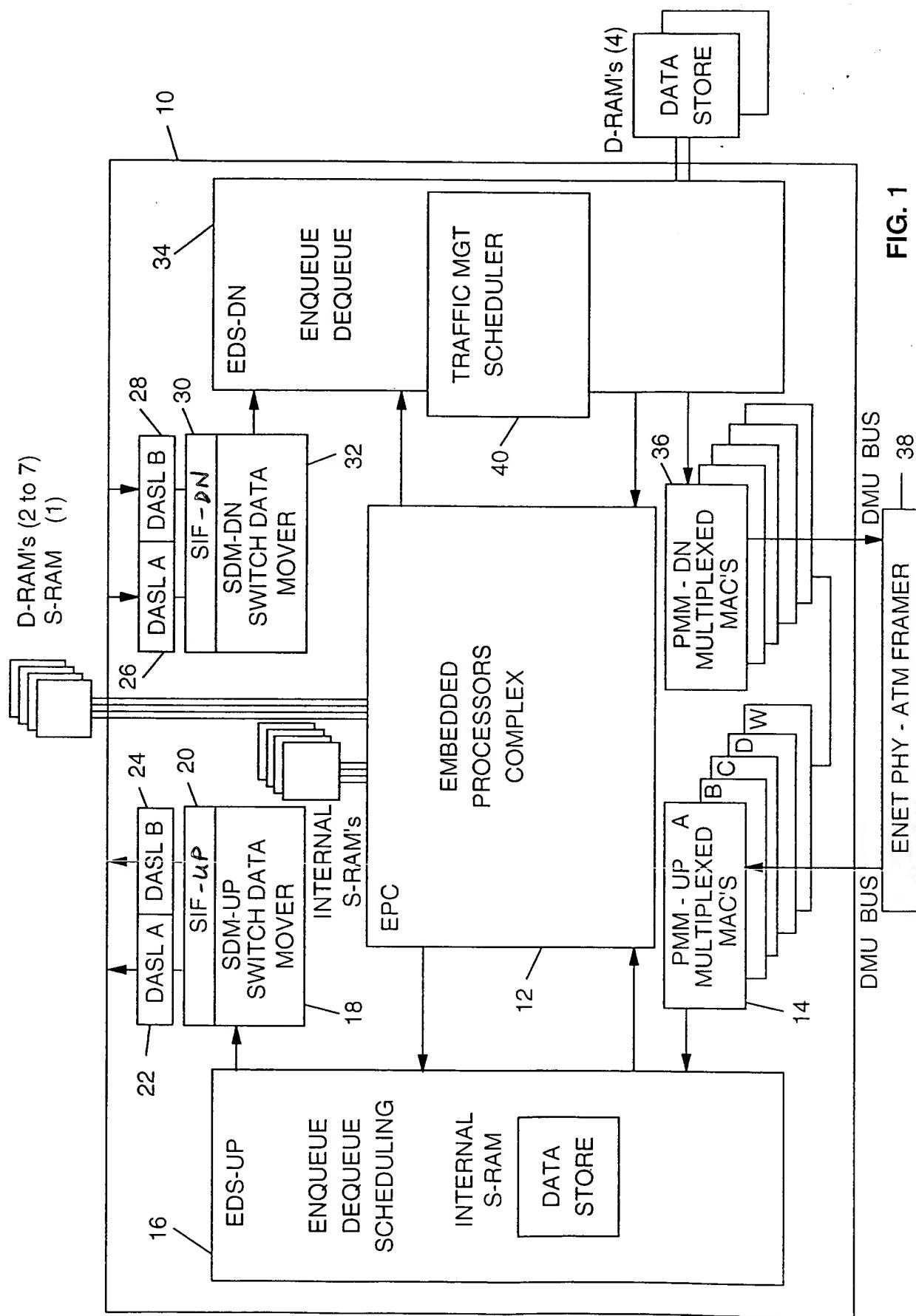


FIG. 1

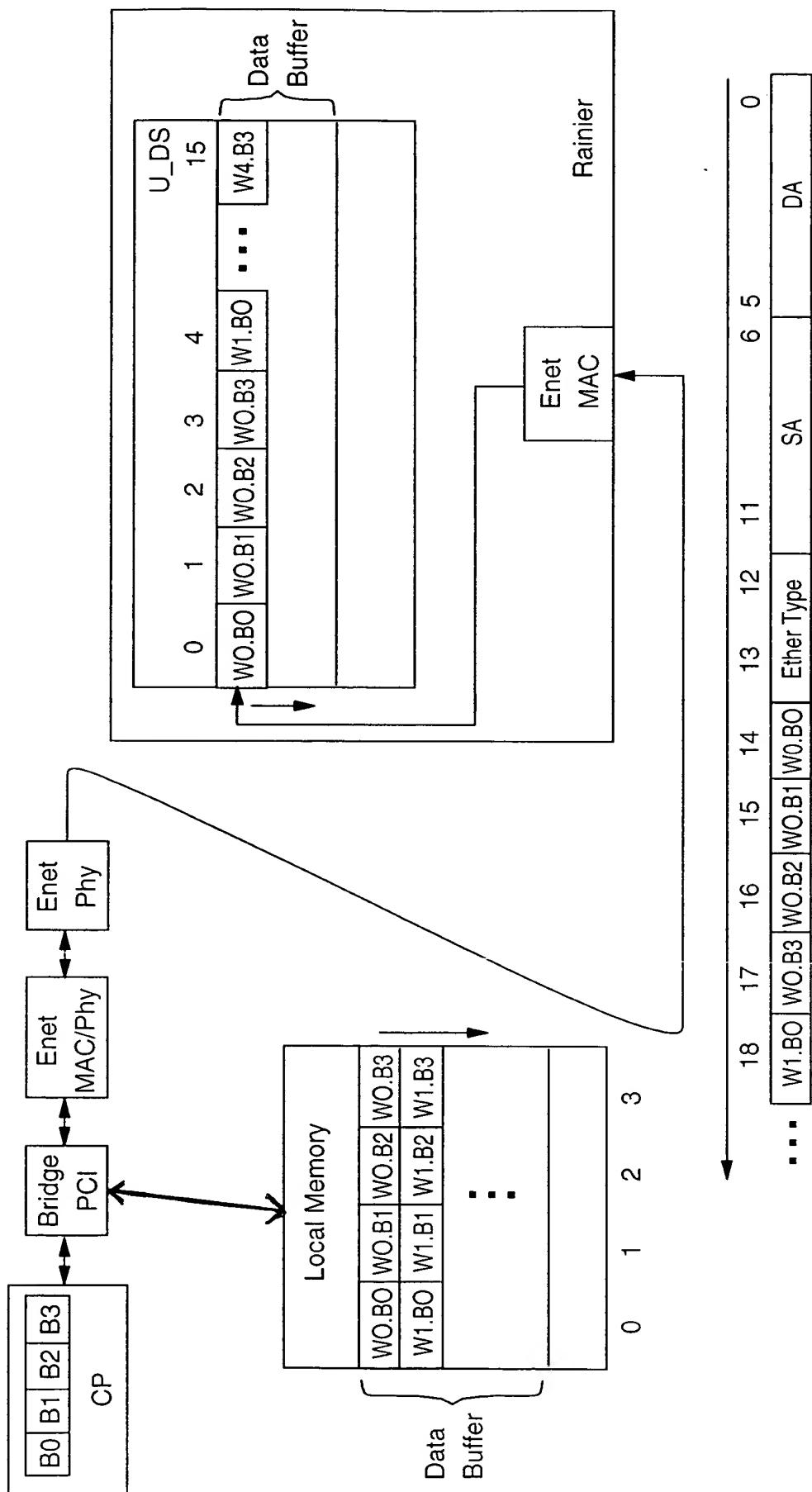


FIG. 3 Guided Traffic Data Flow (Ethernet Port)

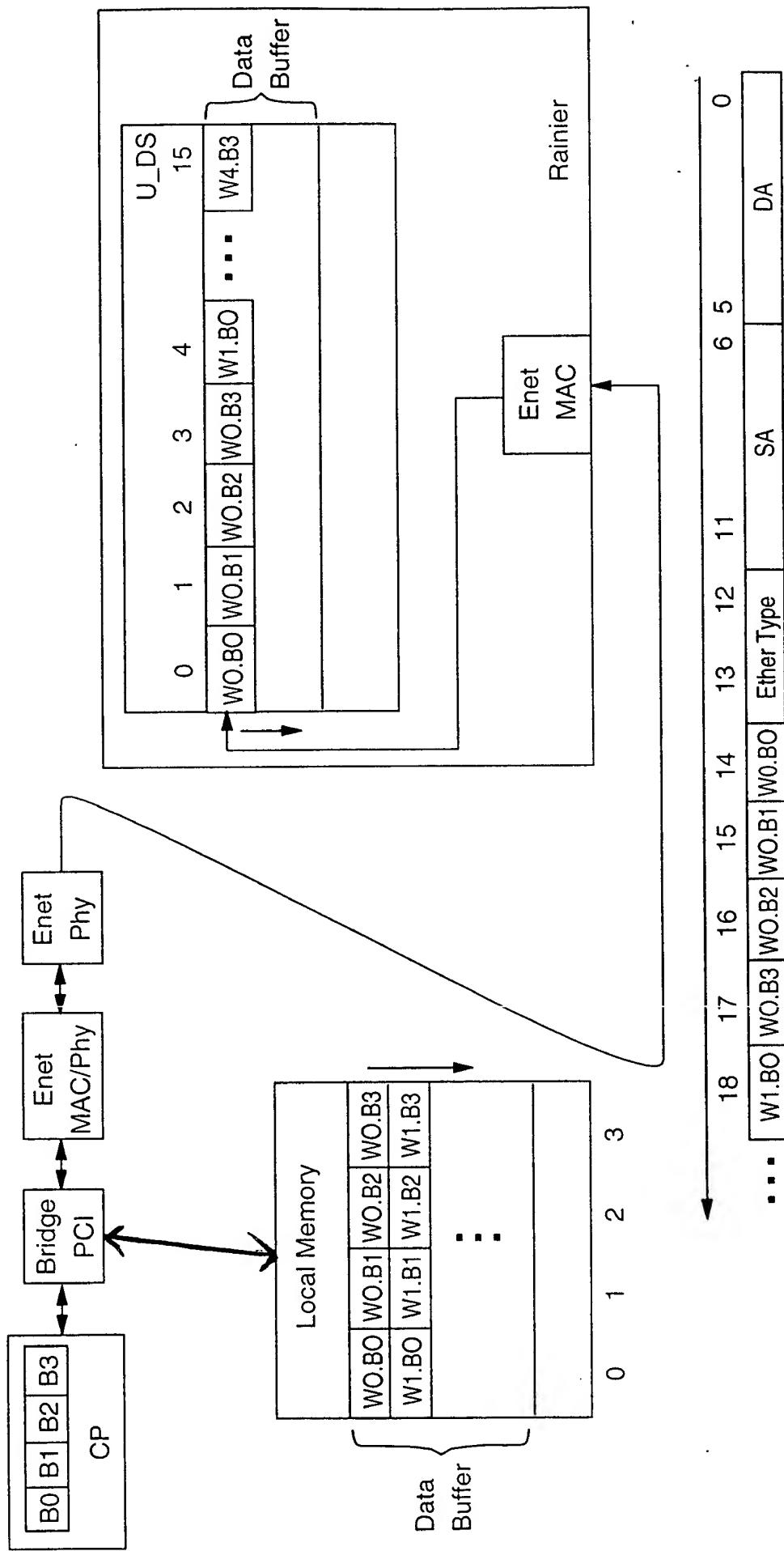


FIG. 3 Guided Traffic Data Flow (Ethernet Port)

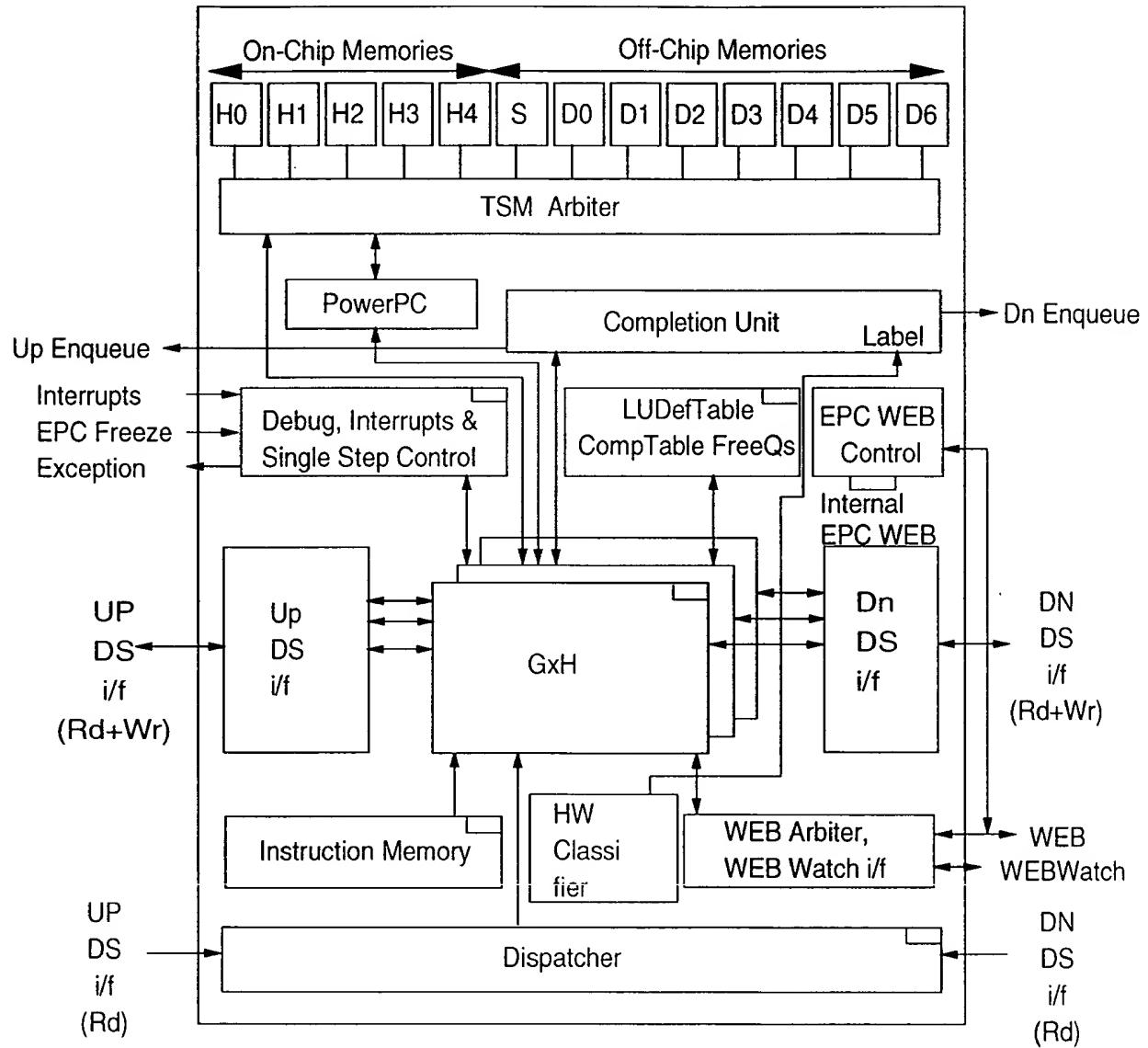


FIG. 12A BLOCK DIAGRAM OF THE EPC

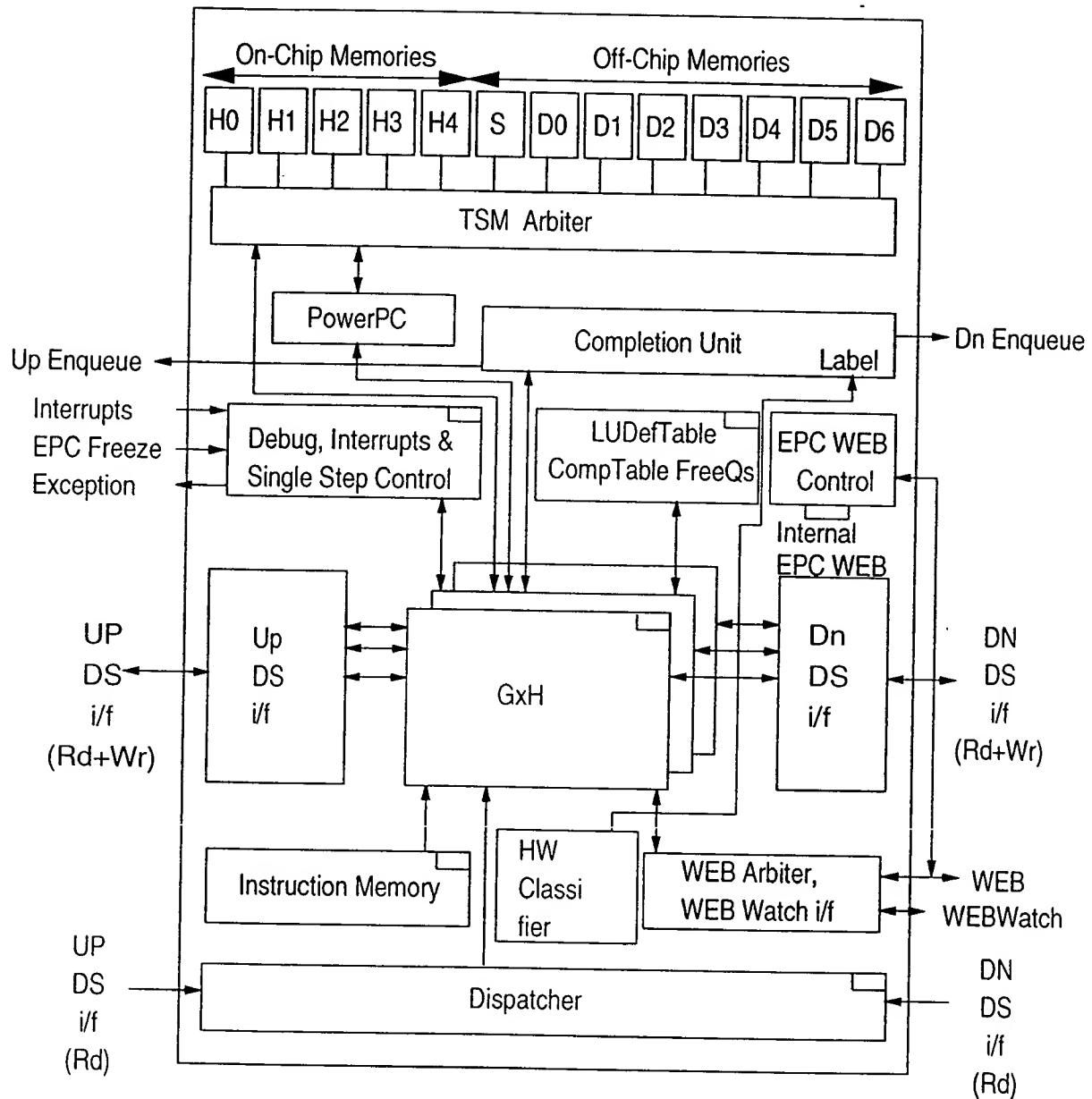


FIG. 12A BLOCK DIAGRAM OF THE EPC

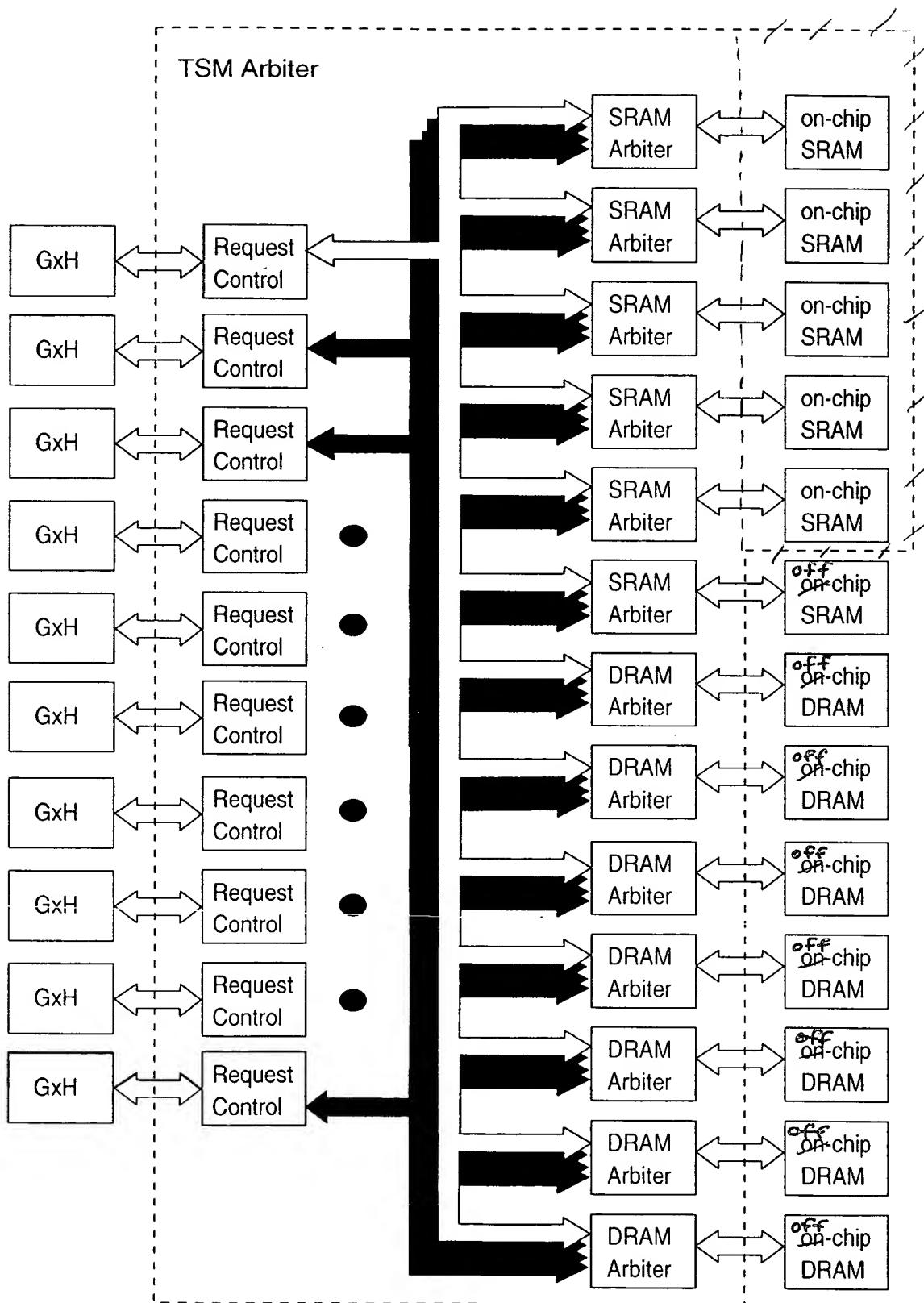


FIG. 13 (Memory Complex)

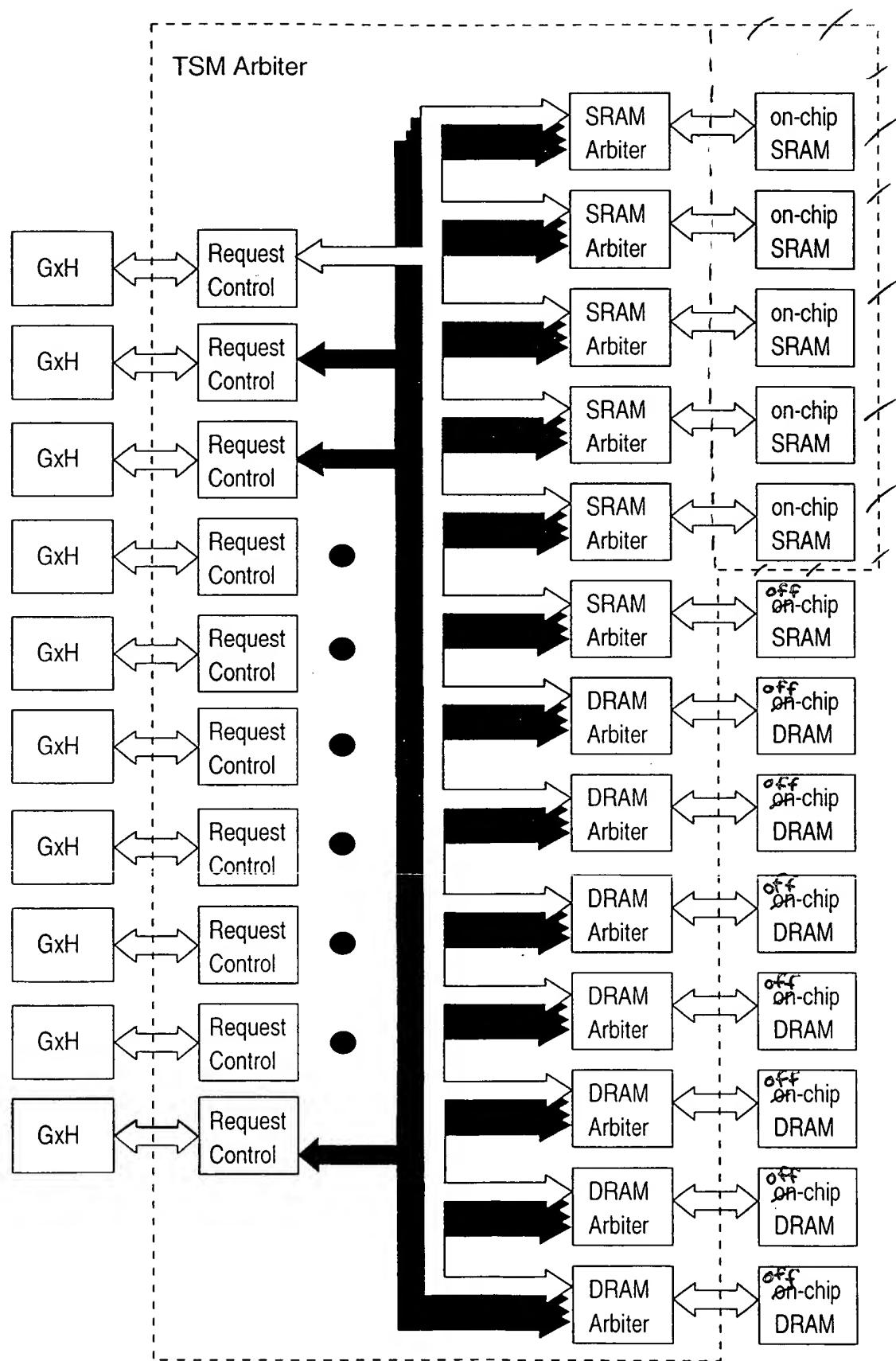


FIG. 13 (Memory Complex)